EXAMINER'S AMENDMENT

1.   An examiner's amendment to the record appears below. Should

the changes and/or additions be unacceptable to applicant, an

amendment may be filed as provided by 37 CFR 1.312. To ensure

consideration of such an amendment, it MUST be submitted no

later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a

telephone interview with Kimton Eng (Reg. No. 43,605) on

3/15/08.

The application has been amended as follows:

In the claims

2.   Please add the following new claims:

Claim 43.   (NEW) The memory module of claim 1 wherein the memory

device interface comprises:

a memory controller coupled to the switch through a memory

controller bus and further coupled to the memory devices through

a memory device bus;

a write buffer coupled to the memory controller for storing

memory requests directed to at least one of the memory devices

to which the memory controller is coupled; and

a cache coupled to the memory controller for storing data provided to the memory devices or retrieved from the memory devices.

Claim 44. (NEW) The memory hub of claim 8, wherein the memory device interface comprises:

a memory controller coupled to the switch through a memory controller bus and further coupled to the memory devices through a memory device bus;

a write buffer coupled to the memory controller for storing memory requests directed to at least one of the memory devices to which the memory controller is coupled; and

a cache coupled to the memory controller for storing data provided to the memory devices or retrieved from the memory devices.

Claim 45. (NEW) The memory system of claim 13, wherein the memory device interface comprises:

a memory controller coupled to the switch through a memory controller bus and further coupled to the memory devices through a memory device bus;

a write buffer coupled to the memory controller for storing
memory requests directed to at least one of the memory devices
to which the memory controller is coupled; and

a cache coupled to the memory controller for storing data
provided to the memory devices or retrieved from the memory
devices.

Claim 45. (NEW) The memory system of claim 13, wherein the
memory device interface comprises:

a memory controller coupled to the switch through a memory
controller bus and further coupled to the memory devices through
a memory device bus;

a write buffer coupled to the memory controller for storing
memory requests directed to at least one of the memory devices
to which the memory controller is coupled; and

a cache coupled to the memory controller for storing data
provided to the memory devices or retrieved from the memory
devices.

Claim 46. (NEW) The computer system of claim 24, wherein the
memory device interface comprises:

a memory controller coupled to the switch through a memory controller bus and further coupled to the memory devices through a memory device bus;

a write buffer coupled to the memory controller for storing memory requests directed to at least one of the memory devices to which the memory controller is coupled; and

a cache coupled to the memory controller for storing data provided to the memory devices or retrieved from the memory devices.

Remarks concerning Examiner's Amendment

3.   The Examiner's Amendment above, initiated by a request from

the applicant, was made to reinstate original claims 3,10,19,

and 30 that were erroneously cancelled by the Examiner in the

Notice of Allowance mailed 1/15/08. Original claims 3,10,19,

and 30 were subject to a restriction requirement based on

species. Pursuant to MPEP 809.02(a) Upon the allowance of a

generic claim, applicant will be entitled to consideration of

claims to additional species which depend from or otherwise

require all the limitations of an allowable generic claim as

provided by 37 CFR 1.141.

Information Disclosure Statement

4.   The submission is in compliance with the provisions of 37

CFR 1.97. Accordingly, the information disclosure statement is

being considered by the examiner.

Examiner's Remarks concerning IDS

5.   The IDS filed 4/10/08 has been fully considered by the

Exmaminer. The prior art cited in the above IDS does not raise

a prima facie case of unpatentability with respect to the

allowed claims.

Conclusion

    Any inquiry concerning this communication or earlier
communications from the examiner should be directed to ERON J.
SORRELL whose telephone number is (571)272-4160. The examiner
can normally be reached on Monday-Friday 8:00AM - 4:30PM.

    If attempts to reach the examiner by telephone are
unsuccessful, the examiner's supervisor, Tariq Hafiz can be
reached on 571-272-6729. The fax phone number for the
organization where this application or proceeding is assigned is
571-273-8300.

    Information regarding the status of an application may be
obtained from the Patent Application Information Retrieval
(PAIR) system. Status information for published applications
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like assistance from a USPTO Customer Service Representative or
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(IN USA OR CANADA) or 571-272-1000.

/Eron J Sorrell/
Examiner, Art Unit 2182
4/14/2008